

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application: Brown et al

Serial No.: 10/064,301

Filed: 1 July 2002

Today's Date: 10 January 2003

Group Art Unit: 2813

Examiner: Nguyen, Thanh T

6/A
1-15-03
JWS.Title: LOGIC SOI STRUCTURE, PROCESS AND APPLICATION FOR VERTICAL
BIPOLAR TRANSISTOR

FAX RECEIVED

AMENDMENT UNDER 35 U.S.C. § 132

JAN 10 2003

To the Assistant Commissioner of Patents
Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Dear Sir:

In response to the Office Action of 24 October 2002, in which all of pending claims 1-10 stand rejected, kindly amend the subject U.S. patent application as follows. Applicants do not believe that any fee is due for the submission of this amendment. However, the Patent Office is hereby authorized to charge Applicants' Deposit Account 09-0456 such fees as may be due for the entry and consideration of this amendment.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

MAIL

☐ deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: _____

FACSIMILE

☒ transmitted by facsimile to the Patent and Trademark Office. 703-872-9318

PAT BLAIR

Name

Pat Blair

Signature

BUR920010185US1

1

S/N 10/064,301